

**REMARKS**

At the outset, the Examiner is thanked for the thorough review and consideration of the pending application. The final Office Action dated November 9, 2010 has been received and its contents carefully reviewed.

Claim 1 has been amended and claim 3 has been canceled. No new matter has been added. Therefore, claims 1-2 and 4-31 are currently pending, of which claims 8-31 are withdrawn from consideration. Reexamination and reconsideration of the pending claims are respectfully requested.

In the final Office Action, **claims 1-3** are rejected under 35 U.S.C. § 103(a) as being unpatentable over Komiya (U.S. Patent Application Publication No. 2002/0158587, hereinafter referred as Komiya) in view of Kochever (U.S. Patent No. 2890332, hereinafter referred as Kochever), Gao et al. (US Pub: 2002/0051893, hereinafter referred as Gao), and Miyazawa et al. (US Patent: 6858991, hereinafter as Miyazawa), and **claims 4-7** are rejected under 35 U.S.C. § 103(a) as being unpatentable over Komiya in view of Kochever and Gao and Miyazawa, as applied in claims 1-3, further in view of Morosawa (U.S. Patent Application Publication No. 2006/0139251, hereinafter referred as Morosawa). These rejections are respectfully traversed and reconsideration is requested.

Claim 1 is allowable over the cited references in that claim 1 recites a combination of elements including, for example, " a plurality of drive voltage supply lines; a plurality of compensation voltage supply lines; EL cells at each crossing of a plurality of data lines and a plurality of gate lines in a matrix, wherein the EL cells emit light in response to currents applied from the drive voltage supply lines; driving thin film transistors connected between the EL cells and compensation voltage supply lines that control the current applied to the EL cells; and a bias switch, connected between a N-1th compensation voltage supply line of the plurality of compensation voltage supply lines and a control terminal of a Nth driving TFT connected to a Nth compensation voltage supply line of the plurality of compensation voltage supply lines to apply a negative bias voltage to the Nth driving TFT connected to the Nth compensation voltage supply line, thereby compensating for a change of threshold voltage of the Nth driving TFT when a scan pulse is supplied to a N-1th gate line of the plurality of gate lines, wherein the bias

switch is controlled by the scan pulse supplied to the N-1th gate line, and wherein the bias switch includes: a control terminal connected to the N-1th gate line; a first input terminal connected to the N-1th compensation voltage supply line; and a second input terminal connected to the control terminal of the Nth driving TFT that is connected to the Nth compensation voltage supply line". None of the cited references, singly or in combination, teaches or suggests at least these features of the claimed invention. Accordingly, Applicants respectfully submit that claim 1 and claims 2, and 4-7, which depend therefrom, are allowable over the cited references.

On page 5 of the final Office Action, the Examiner asserted that Komiya teaches "wherein the bias switch includes a control terminal connected to the N-1th gate line (e.g. gate line 1); a first input terminal connected to the N-1th compensation voltage supply line (e.g., the first top VEE); and a second input terminal connected to the control terminal of the driving TFT that is connected to the Nth compensation voltage supply line (e.g., the bias switch 31 is connected to the next gate line which reside between the tow neighboring cell)(see Komiya, Fig. 5, [0043], [0044]."

However, Applicants submit Komiya does not disclose n compensation voltage supply lines including N-1th and Nth compensation voltage supply lines, but discloses only one compensation voltage supply line VEE as shown in figure 5. Furthermore, Komiya fails to disclose the bias switch including a control terminal connected to the N-1th gate line, a first input terminal connected to the N-1th compensation voltage supply line, and a second input terminal connected to the control terminal of the Nth driving TFT that is connected to the Nth compensation voltage supply line. Komiya fails to teach or suggest the bias switch including the second input terminal connected to the control terminal of the Nth driving TFT that is connected to the Nth compensation voltage supply line because the TFT3 of Komiya includes a second input connected with of source or drain terminal of the Nth driving TFT (TFT2) which is not connected with the Nth compensation voltage supply line but is connected with the EL element (EL).

Accordingly, Applicants respectfully submit that claim 1, and claims 2, and 4-7, which depend from claim 1 are patentable over Komiya, Kochevar, Gao, Miyazawa and Morosawa because no combination of them teaches, either expressly or inherently, at least these features of the claimed invention.

In view of the above, each of the presently pending claims in this application is believed to be in immediate condition for allowance. Accordingly, the Examiner is respectfully requested to pass this application to issue.

Applicants believe the application is in condition for allowance and early, favorable action is respectfully solicited. If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at (202) 496-7500 to discuss the steps necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time, or any other fees required to complete the filing this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911. A duplicate copy of this sheet is enclosed.

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Respectfully submitted,

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